		Application No.	Applicant(s)
Notice of Allowability		10/722,397	MOURI ET AL.
		Examiner	Art Unit
		Adam R. Giesy	2627

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to <u>7/19/2007</u>.

2. ☒ The allowed claim(s) is/are <u>6-19,21 and 22 renumbered as 1-16</u>.

3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

    a) ☒ All   b) ☐ Some*   c) ☐ None   of the:

        1. ☒ Certified copies of the priority documents have been received.

        2. ☐ Certified copies of the priority documents have been received in Application No. _____ .

        3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

    * Certified copies not received: _____ .

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.

5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.

    (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached

        1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____ .

    (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of

    Paper No./Mail Date _____ .

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).

6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)

2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)

3. ☐ Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date _____

4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material

5. ☐ Notice of Informal Patent Application

6. ☐ Interview Summary (PTO-413), Paper No./Mail Date _____ .

7. ☒ Examiner's Amendment/Comment

8. ☐ Examiner's Statement of Reasons for Allowance

9. ☐ Other _____ .

WAYNE YOUNG
SUPERVISORY PATENT EXAMINER

EXAMINER'S AMENDMENT

1.   An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

2.   Authorization for this examiner's amendment was given in a telephone interview with Jeffery R. Filipek on 9/20/2007.

3.   The application has been amended as follows:

In the claims:

In claim 6, line 5 of the claim, "...WBL binarization..." has been changed to – ...Wobble (WBL) binarization... --.

In claim 6, line 7 of the claim, "a FEP (Front End Processor)..." has been changed to – a Front End Processor (FEP)... --.

In claim 6, line 9 of the claim, "an ADC (Analog-to-Digital Converter)..." has been changed to – an Analog-to-Digital Converter (ADC)... --.

In claim 6, line 11 of the claim, "...an ADIP (Address In Pre-Groove)..." has been changed to – ...an Address In Pre-Groove (ADIP)... --.

In claim 6, line 18 of the claim, "a PLL (Phase Locked Loop)..." has been changed to – a Phase Locked Loop (PLL)... --.

In claim 6, line 25 of the claim, "a PRML (Partial Response Maximum Likelihood)..." has been changed to – a Partial Response Maximum Likelihood (PRML)... --.

In claim 8, line 5 of the claim, "...WBL binarization..." has been changed to –

...Wobble (WBL) binarization... --.

In claim 8, line 7 of the claim, "a FEP (Front End Processor)..." has been

changed to – a Front End Processor (FEP)... --.

In claim 8, line 9 of the claim, "an ADC (Analog-to-Digital Converter)..." has

been changed to – an Analog-to-Digital Converter (ADC)... --.

In claim 8, line 11 of the claim, "...an ADIP (Address In Pre-Groove)..." has been

changed to – ...an Address In Pre-Groove (ADIP)... --.

In claim 8, line 18 of the claim, "a PLL (Phase Locked Loop)..." has been

changed to – a Phase Locked Loop (PLL)... --.

In claim 8, line 24 of the claim, "a PRML (Partial Response Maximum

Likelihood)..." has been changed to – a Partial Response Maximum Likelihood

(PRML)... --.

In claim 10, line 5 of the claim, "...WBL binarization..." has been changed to –

...Wobble (WBL) binarization... --.

In claim 10, line 7 of the claim, "a FEP (Front End Processor)..." has been

changed to – a Front End Processor (FEP)... --.

In claim 10, line 9 of the claim, "an ADC (Analog-to-Digital Converter)..." has

been changed to – an Analog-to-Digital Converter (ADC)... --.

In claim 10, line 11 of the claim, "...an ADIP (Address In Pre-Groove)..." has

been changed to – ...an Address In Pre-Groove (ADIP)... --.

In claim 10, line 18 of the claim, "a PLL (Phase Locked Loop)..." has been changed to – a Phase Locked Loop (PLL)... --.

In claim 10, line 25 of the claim, "a PRML (Partial Response Maximum Likelihood)..." has been changed to – a Partial Response Maximum Likelihood (PRML)... --.

In claim 11, line 5 of the claim, "...WBL binarization..." has been changed to – ...Wobble (WBL) binarization... --.

In claim 11, line 7 of the claim, "a FEP (Front End Processor)..." has been changed to – a Front End Processor (FEP)... --.

In claim 11, line 9 of the claim, "an ADC (Analog-to-Digital Converter)..." has been changed to – an Analog-to-Digital Converter (ADC)... --.

In claim 11, line 11 of the claim, "...an ADIP (Address In Pre-Groove)..." has been changed to – ...an Address In Pre-Groove (ADIP)... --.

In claim 11, line 18 of the claim, "a PLL (Phase Locked Loop)..." has been changed to – a Phase Locked Loop (PLL)... --.

In claim 12, line 5 of the claim, "...WBL binarization..." has been changed to – ...Wobble (WBL) binarization... --.

In claim 12, line 7 of the claim, "a FEP (Front End Processor)..." has been changed to – a Front End Processor (FEP)... --.

In claim 12, line 9 of the claim, "an ADC (Analog-to-Digital Converter)..." has been changed to – an Analog-to-Digital Converter (ADC)... --.

In claim 12, line 11 of the claim, "...an ADIP (Address In Pre-Groove)..." has been changed to – ...an Address In Pre-Groove (ADIP)... --.

In claim 12, line 18 of the claim, "a PLL (Phase Locked Loop)..." has been changed to – a Phase Locked Loop (PLL)... --.

In claim 14, line 5 of the claim, "...WBL binarization..." has been changed to – ...Wobble (WBL) binarization... --.

In claim 14, line 7 of the claim, "a FEP (Front End Processor)..." has been changed to – a Front End Processor (FEP)... --.

In claim 14, line 9 of the claim, "an ADC (Analog-to-Digital Converter)..." has been changed to – an Analog-to-Digital Converter (ADC)... --.

In claim 14, line 11 of the claim, "...an ADIP (Address In Pre-Groove)..." has been changed to – ...an Address In Pre-Groove (ADIP)... --.

In claim 14, line 18 of the claim, "a PLL (Phase Locked Loop)..." has been changed to – a Phase Locked Loop (PLL)... --.

In claim 14, line 25 of the claim, "a DSV (Digital Sum Value)..." has been changed to – a Digital Sum Value (DSV)... --.

In claim 15, line 5 of the claim, "...WBL binarization..." has been changed to – ...Wobble (WBL) binarization... --.

In claim 15, line 7 of the claim, "a FEP (Front End Processor)..." has been changed to – a Front End Processor (FEP)... --.

In claim 15, line 9 of the claim, "an ADC (Analog-to-Digital Converter)..." has been changed to – an Analog-to-Digital Converter (ADC)... --.

In claim 15, line 11 of the claim, "...an ADIP (Address In Pre-Groove)..." has

been changed to – ...an Address In Pre-Groove (ADIP)... --.

In claim 15, line 18 of the claim, "a PLL (Phase Locked Loop)..." has been

changed to – a Phase Locked Loop (PLL)... --.

In claim 17, line 5 of the claim, "...WBL binarization..." has been changed to –

...Wobble (WBL) binarization... --.

In claim 17, line 7 of the claim, "a FEP (Front End Processor)..." has been

changed to – a Front End Processor (FEP)... --.

In claim 17, line 9 of the claim, "an ADC (Analog-to-Digital Converter)..." has

been changed to – an Analog-to-Digital Converter (ADC)... --.

In claim 17, line 11 of the claim, "...an ADIP (Address In Pre-Groove)..." has

been changed to – ...an Address In Pre-Groove (ADIP)... --.

In claim 17, line 18 of the claim, "a PLL (Phase Locked Loop)..." has been

changed to – a Phase Locked Loop (PLL)... --.

In claim 17, line 23 of the claim, "...an AGC (Auto Gain Control)..." has been

changed to – ...an Auto Gain Control (AGC)... --.

In claim 18, line 5 of the claim, "...WBL binarization..." has been changed to –

...Wobble (WBL) binarization... --.

In claim 18, line 7 of the claim, "a FEP (Front End Processor)..." has been

changed to – a Front End Processor (FEP)... --.

In claim 18, line 9 of the claim, "an ADC (Analog-to-Digital Converter)..." has

been changed to – an Analog-to-Digital Converter (ADC)... --.

In claim 18, line 11 of the claim, "…an ADIP (Address In Pre-Groove)…" has

been changed to – …an Address In Pre-Groove (ADIP)… --.

In claim 18, line 18 of the claim, "a PLL (Phase Locked Loop)…" has been

changed to – a Phase Locked Loop (PLL)… --.

Allowable Subject Matter

4.   Claims 6-19, 21, and 22 are allowed over the prior art of record for the reasons
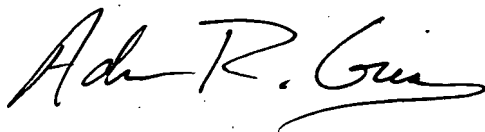
stated in the previous Office Action, mailed on 8/23/2006.

Conclusion

5.   Any inquiry concerning this communication or earlier communications from the
examiner should be directed to Adam R. Giesy whose telephone number is (571) 272-
7555. The examiner can normally be reached on 8:00am- 5:30pm.

        If attempts to reach the examiner by telephone are unsuccessful, the examiner's
supervisor, Wayne R. Young can be reached on (571) 272-7582. The fax phone
number for the organization where this application or proceeding is assigned is 571-
273-8300.

        Information regarding the status of an application may be obtained from the
Patent Application Information Retrieval (PAIR) system. Status information for
published applications may be obtained from either Private PAIR or Public PAIR.
Status information for unpublished applications is available through Private PAIR only.
For more information about the PAIR system, see http://pair-direct.uspto.gov. Should
you have questions on access to the Private PAIR system, contact the Electronic
Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a
USPTO Customer Service Representative or access to the automated information
system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

ARG 9/12/2007

WAYNE YOUNG
SUPERVISORY PATENT EXAMINER